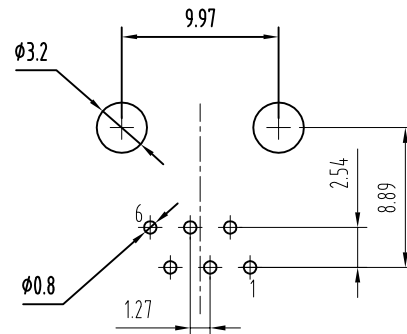
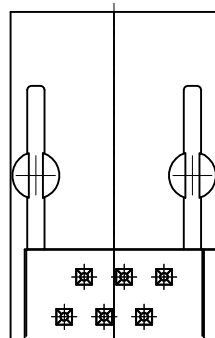
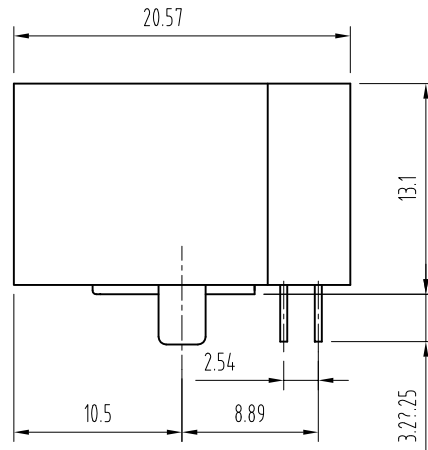
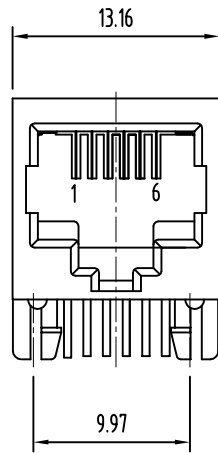


REVISION RECORD				
REV	ECO	DESCRIPTION	DRFT	CHKD



PC Board Layout  
Component Side Shown

NOTES:

MATERIAL :

- 1.HOUSING MATERIAL :GLASS FILLED POLYESTER UL94V-0.
- 2.CONTACT MATERIAL :PHOSPHOR BRONZE  $\phi$ 0.45mm.
- 3.PLATING :GOLD PLATING OVER NICKEL.

ELECTRICAL:

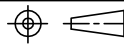
- 1.VOLTAGE RATING :125 VAC RMS.
- 2.CURRENT RATING :1.5 AMP.
- 3.CONTACT RESISTANCE :30 MILLIOHMS MAX.
- 4.INSULATION RESISTANCE :500 MEGOHMS MIN @ 500V DC .
- 5.DIELECTRIC WITHSTANDING RESISTANCE :1000V AC RMS 50Hz. 1MIN.

MECHANICAL:

- 1.DURRABILITY :750 CYCLES MIN.
- 2.PCB RETENTION PRE-SOLDER :1 LB MIN.

ENVIRONMENTAL:

- 1. STORAGE : -40 $^{\circ}$ C~+85 $^{\circ}$ C
- 2. OPERATION : 0? TO 70?.

DETAILED LISTS TOLERANCES EXCEPT AS NOTED MM .0 ± 0.15                      ± .00 ± 0.075                   ± .000 ± 0.05                   ± ANGLES ± 0.5  THIRD ANGLE PROJECTION	DFTO: _____ DATE: _____	<h1 style="text-align: center;">Coorle 库乐科技</h1> <p style="text-align: center;">TITLE 59A JACK W/O SHIELD</p>		
	CHKD: _____ DATE: _____			
	MFO: _____ DATE: _____			
	APPVL: _____ DATE: _____			
MATERIAL :	Q.T'Y :	DRAWING NO. TES9A1001	SIZE A3	REV 0
FINISH :	SCALE :	/PART NO.	DO NOT SCALE DRAWING	
		SHEET X		OF Y